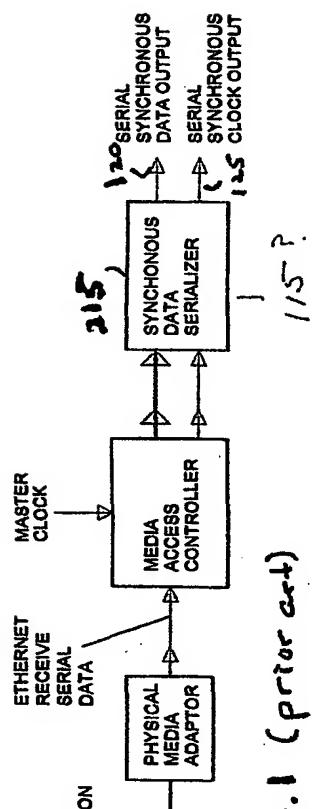


Figure 1, Hierarchical TDM signals-over-Ethernet instantiation.  
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Fig. 1 (prior art)



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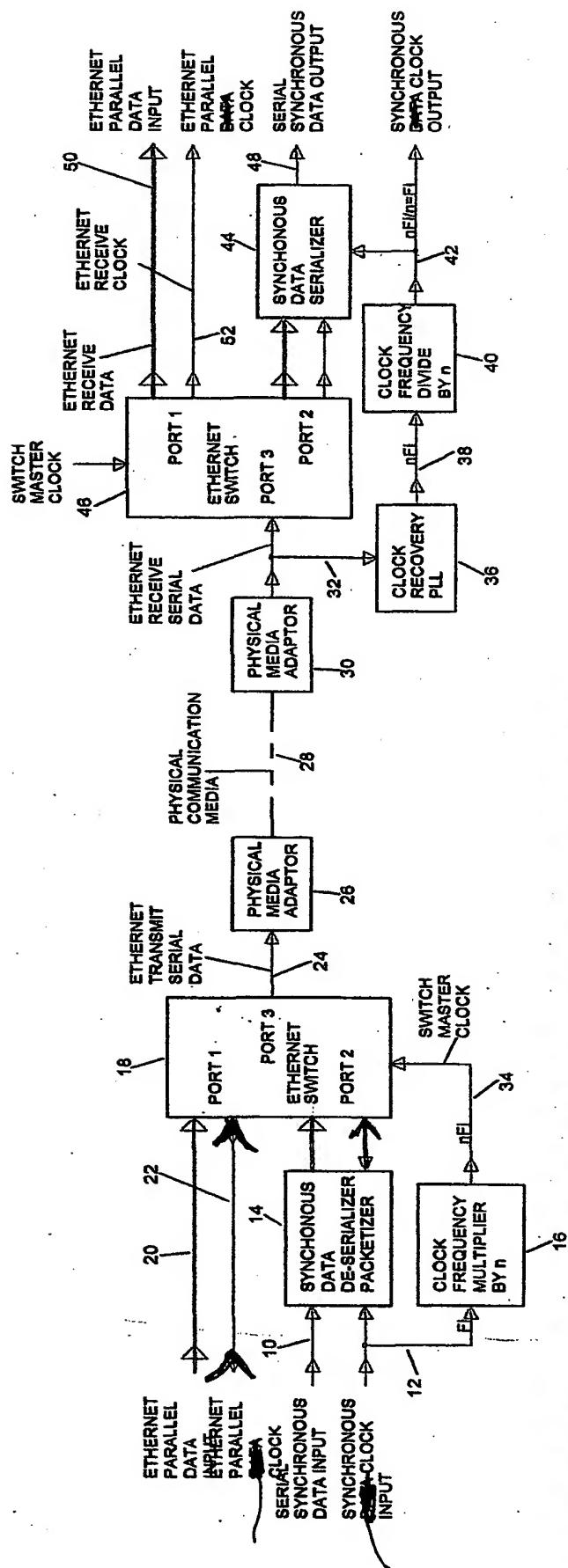


Figure 2, An embodiment of the invention in one direction of transmission.

Fig. 2 (b)

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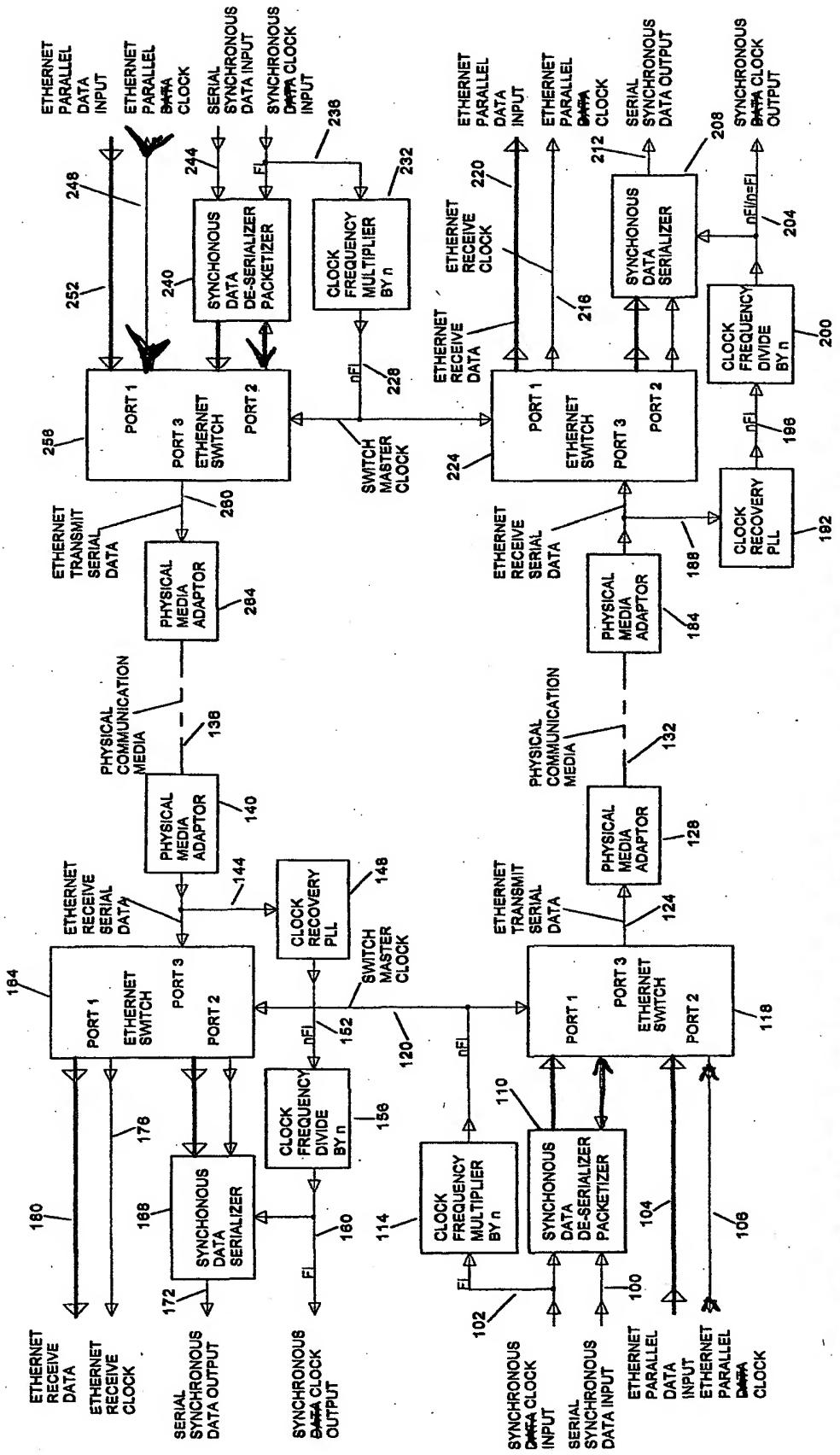


Figure 3. An embodiment of the invention in bi-directional transmission of TDM signals over Ethernet. (3)

F. 6. 3

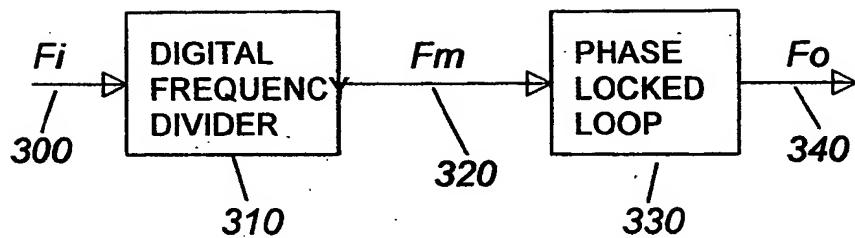


Figure 4, An embodiment of a frequency Multiplier / Divider.

FIG. 4

(4)

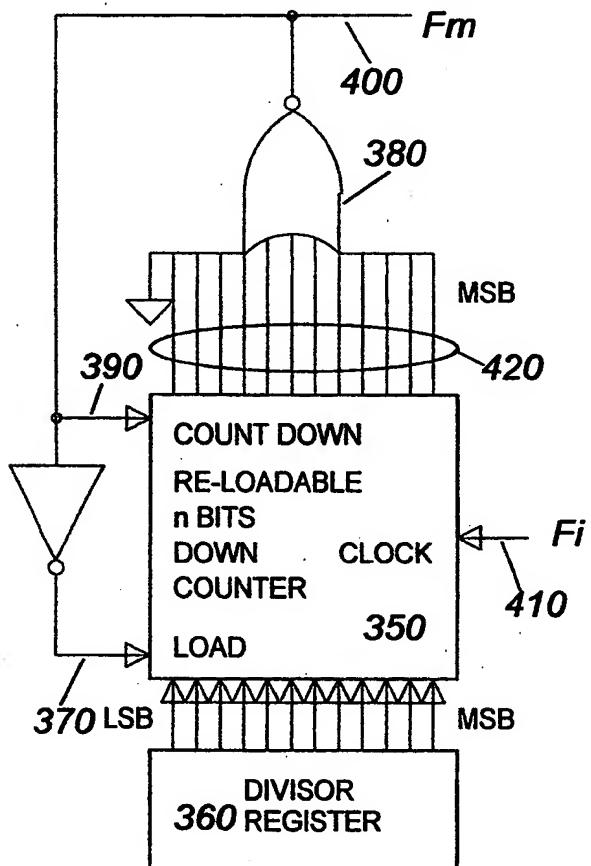


Figure 5, An embodiment of a Digital Frequency Divider.

FIG. 5

(5)

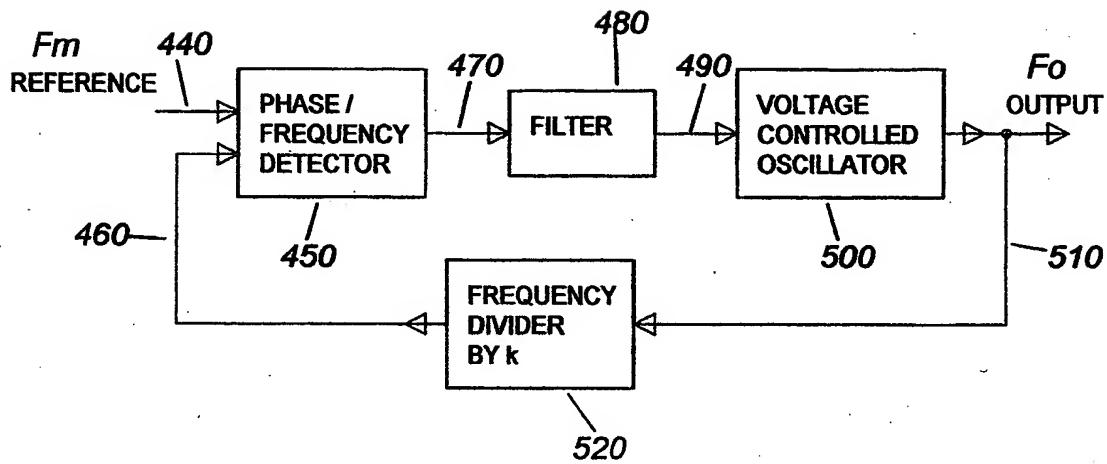


Figure 6, An embodiment of a Phase Locked Loop.

FIG. 6

(6)

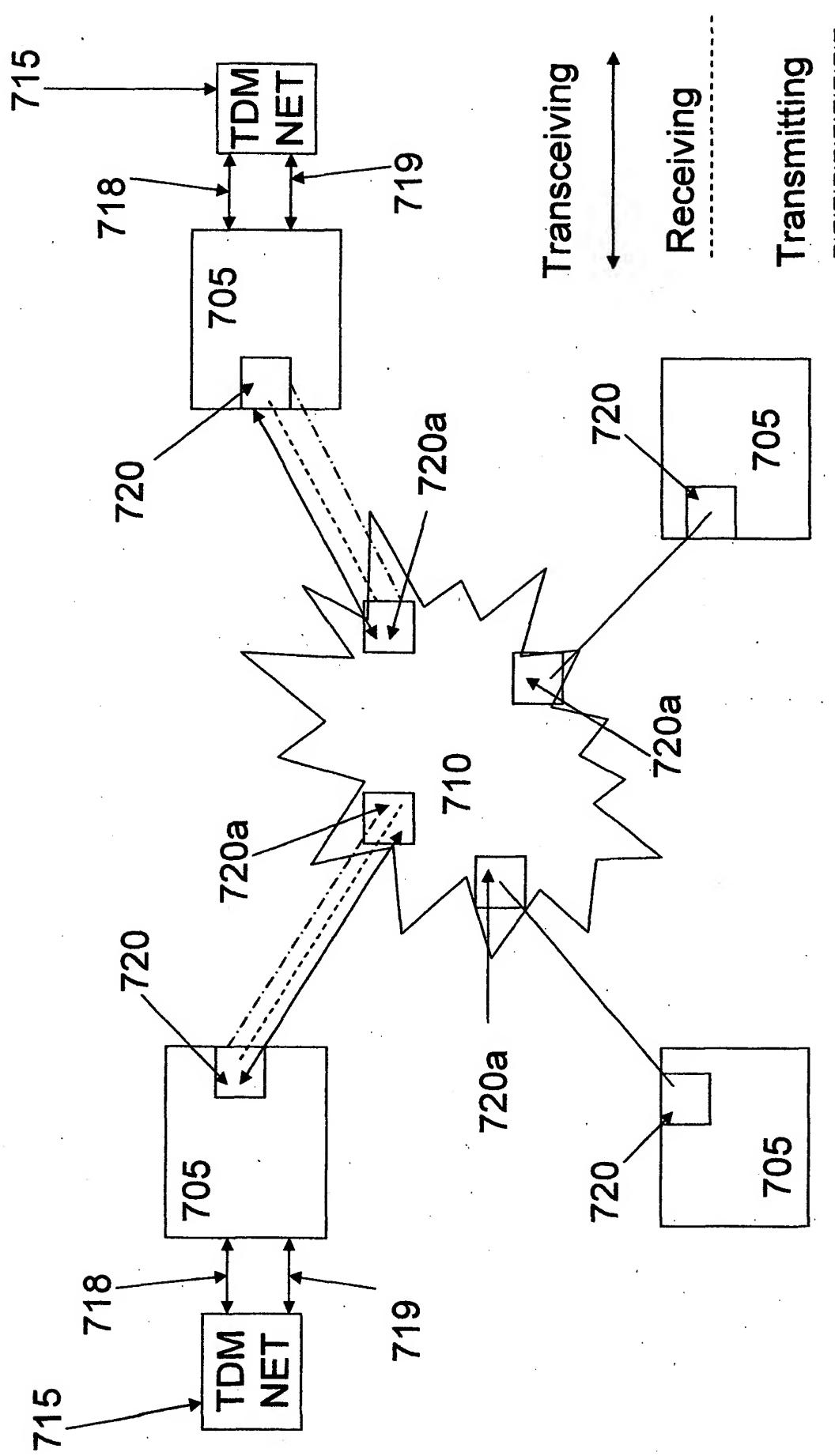


Fig. 7

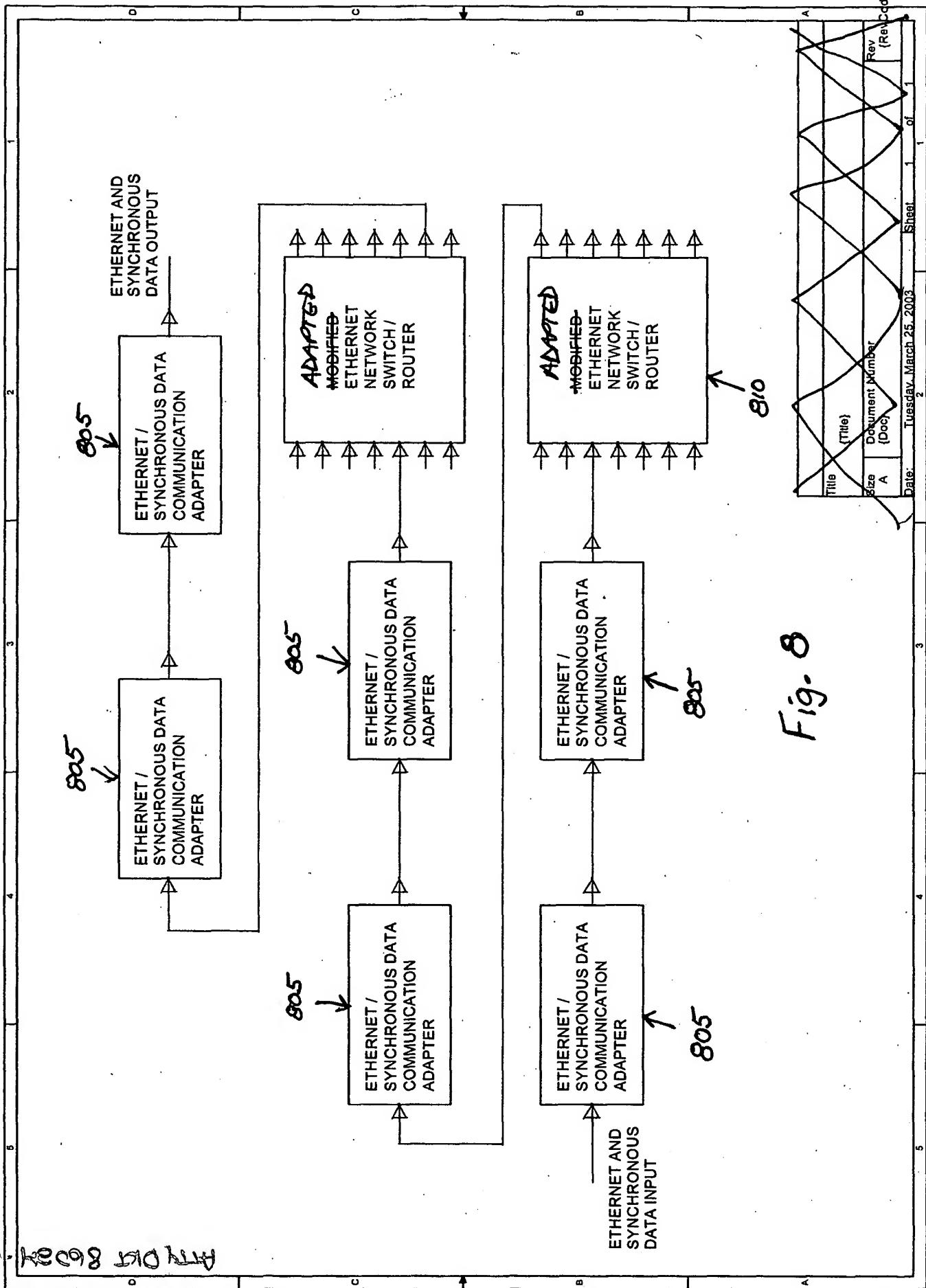


Fig. 8

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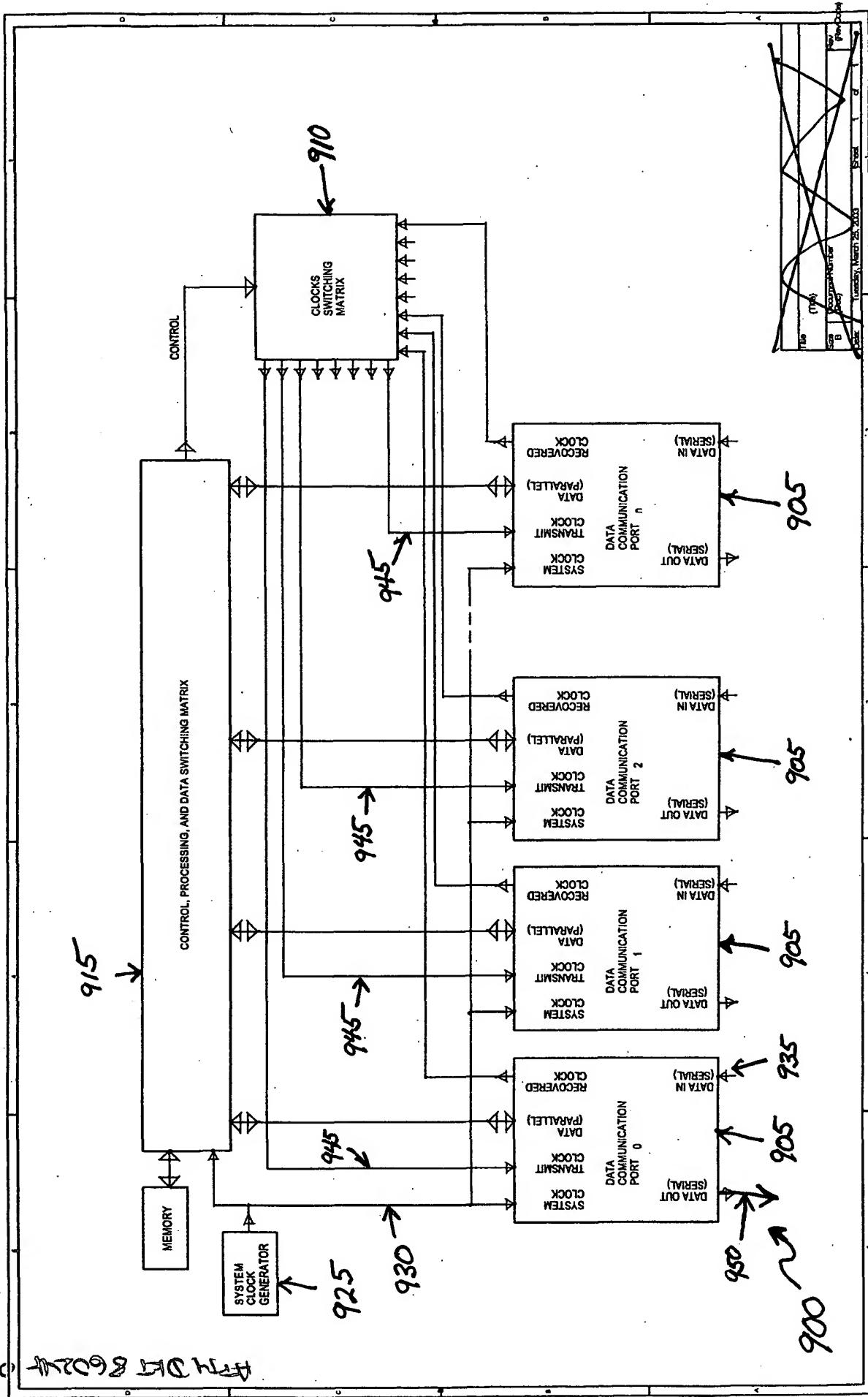


Fig. 9